INFORMATION DISCLOSURE CITATION IN AN APPLICATION			SURE	ATTY. DOCKET NO. <b>57454-206</b>		SERIAL NO.		
				APPLICANT Yasuhiro NUNOMURA				
(PTO-1449)				FILING DATE August 14, 2001		GROUP 25		
		U	S. PATEN	T DOCUMENTS			-	
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME		CLASS	SUBCLASS FILING DA		LING DATE
		FOR	EIGN PAT	ENT DOCUMEN	  TS		<u> </u>	
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY		CLASS	SUBCLASS	Yes	Translation No
杜	8-6681	1/12/1996		English Abstract)				
FF	5-28116	2/5/1993	Japan (w/ I	English Abstract)				
	1000							
	OTHER	R ART (Inch	ding Autho	or, Title, Date, Per	rtinent F	Pages, Etc.)	ــــــــــــــــــــــــــــــــــــــ	<u> </u>
TT.	"Computer Architecture: Design and Performance", B. Wilkinson, April 30, 1994, p. 210.  "Real-Time Task Scheduling for a Variable Voltage Processor", T. OKUMA et al., The Journal of The Institute of Electronics, Information and Communication Engineers, C Vol. J83-C No. 6, June 2000, pp. 454-462.							
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